

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2143

Serial No.: 10/615,280

Examiner : Phuoc H. Nguyen

Filed

: July 8, 2003

Conf. No.: 8226

Assiqnee

: Intel Corporation

Title

: THREAD SIGNALING IN MULTI-THREADED NETWORK PROCESSOR

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Responsive to the action mailed July 1, 2005, applicants elect the Group I claims 1-28, drawn to scheduling multiple threads based upon the register use and current status. election is made without traverse.

Please apply any charges or credits to Deposit Account No. 06-1050.

RespectfuX)y submitted,

July 29, 2005

Harris Reg. No. 32,030

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit

July 29, 2005

Signature

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